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RESPONSE UNDER 37 C.F.R. § 1.116
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To: Commissioner for Patents Technology Center 2800	Total Pages Sent: 6 (including cover sheet)
Facsimile Number: (571) 273-8300	Transmission Date: August 16, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Lin, <i>et al.</i>	Docket No.:	TSM03-0670
Serial No:	10/729,092	Art Unit:	2815
Date Filed:	December 5, 2003	Examiner:	Matthew E. Warren
Title:	Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors		

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- Certification of Facsimile Transmission (1 page)
- Response to Office Action (5 pages)

Respectfully submitted,

Kristy Engeldahl
Legal Assistant

Confirmation Respectfully Requested

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Channel Transistors

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

The following remarks are offered in response to Examiner's Office Action dated June 2, 2006, a final rejection. Applicants respectfully request reconsideration of claims 1-9, 12, 14, 16-17, 22-23, 25-34, 36-38, 40-42, 45-55 and 78-80.